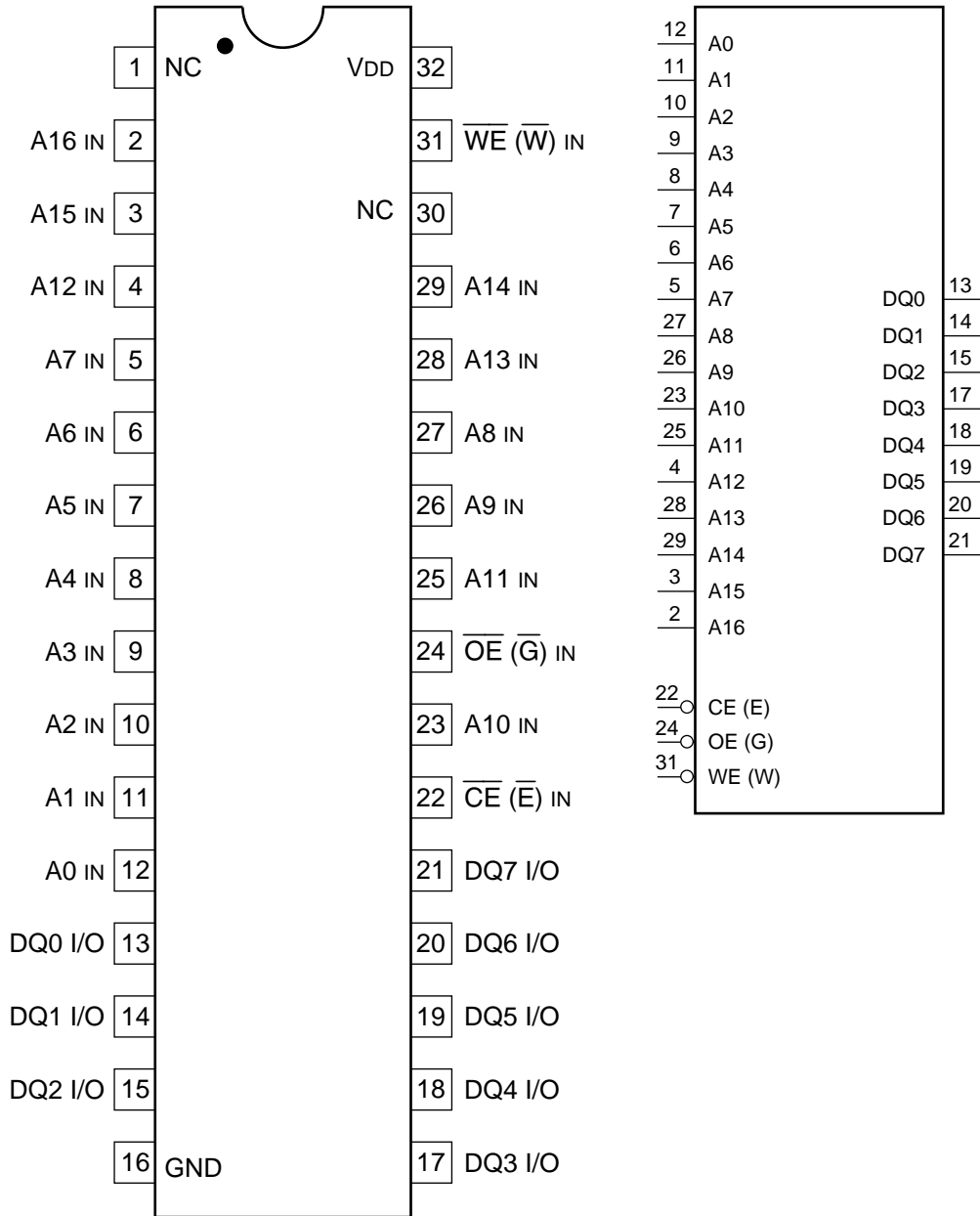


C-MOS 1 M (131072 × 8)-BIT FLASH MEMORY

—TOP VIEW—



**INPUT**

- A0 - A16 ; ADDRESS (0 - 16)
- CE ; CHIP ENABLE
- OE ; OUTPUT ENABLE
- WE ; WRITE ENABLE

**OUTPUT**

- DQ0 - DQ7 ; DATA (0 - 7)

OPERATION	$\overline{CE}$	$\overline{OE}$	$\overline{WE}$	A0	A1	A9	I/O
AUTO-SELECT MANUFACTURER CODE (1)	0	0	1	0	0	VID	CODE
AUTO-SELECT DEVICE CODE (1)	0	0	1	1	0	VID	CODE
READ	0	0	1	A0	A1	A9	DOUT
STANDBY	1	X	X	X	X	X	HI-Z
OUTPUT DISABLE	0	1	1	X	X	X	HI-Z
WRITE	0	1	0	A0	A1	A9	DIN (2)
ENABLE SECTOR PROTECT	0	VID	0	X	X	VID	X
VERIFY SECTOR PROTECT (3)	0	0	1	0	1	VID	CODE

- 0 : LOW LEVEL
- 1 : HIGH LEVEL
- X : DON'T CARE
- HI-Z : HIGH IMPEDANCE

